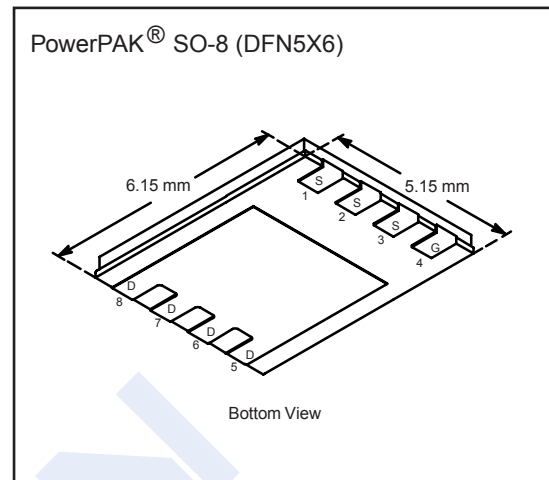
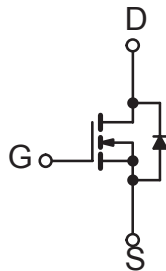


## N-Channel MOSFET

### SIR422DP (KIR422DP)

#### ■ Features

- $V_{DS} (V) = 40V$
- $I_D = 40 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 7.5 m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 9 m\Omega (V_{GS} = 4.5V)$



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	$V_{DS}$	40	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$		
Continuous Drain Current	$I_D$	$T_c=25^\circ C$	40	A
		$T_c=70^\circ C$	40	
		$T_a=25^\circ C$	20.5	
		$T_a=70^\circ C$	16.4	
Pulsed Drain Current	$I_{DM}$	70		
Avalanche Current	$I_{AS}$	30		
Avalanche Energy	$E_{AS}$	45	mJ	
Power Dissipation	$P_D$	$T_c=25^\circ C$	34.7	W
		$T_c=70^\circ C$	22.2	
		$T_a=25^\circ C$	5	
		$T_a=70^\circ C$	3.2	
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	25	$^\circ C/W$	
Thermal Resistance.Junction- to-Case	$R_{thJC}$	3.6		
Junction Temperature	$T_J$	150	$^\circ C$	
Storage Temperature Range	$T_{stg}$	-55 to 150		

## N-Channel MOSFET

### SIR422DP (KIR422DP)

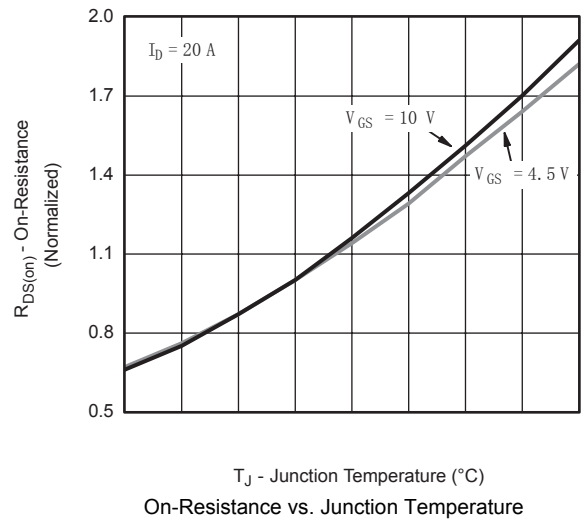
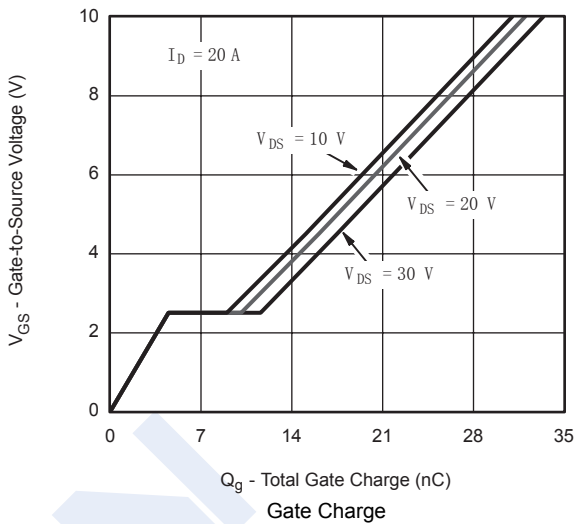
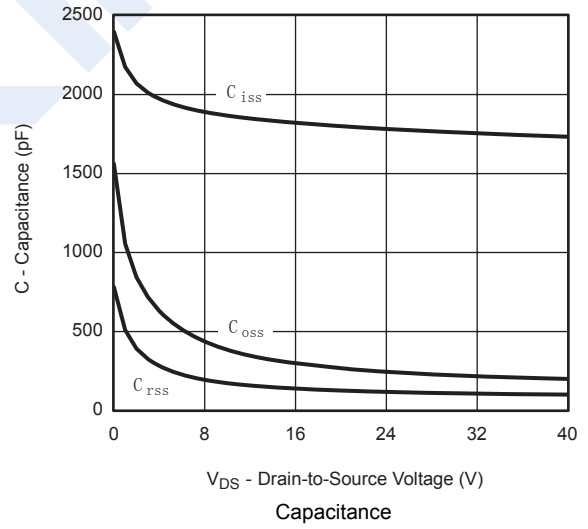
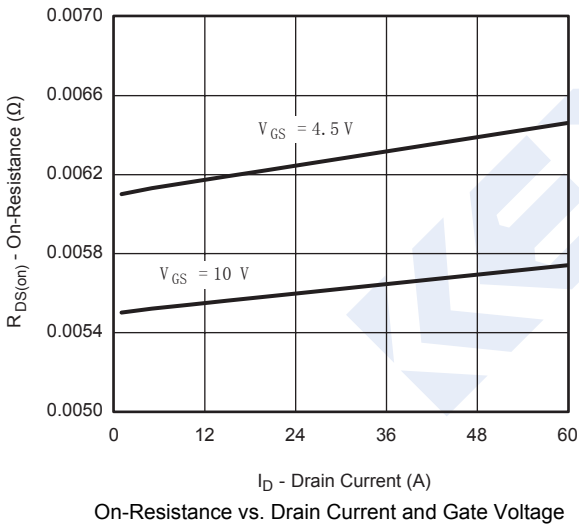
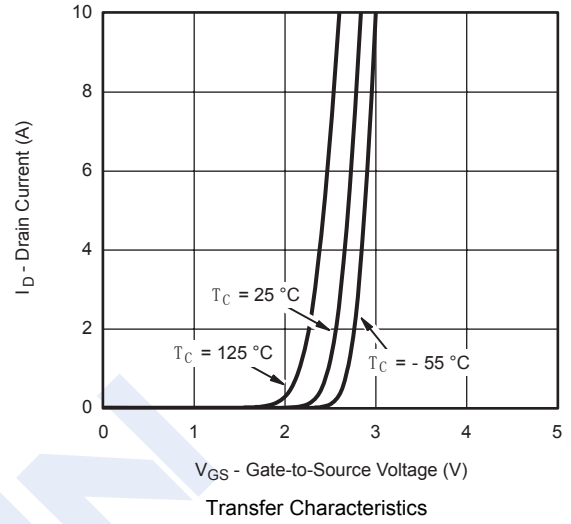
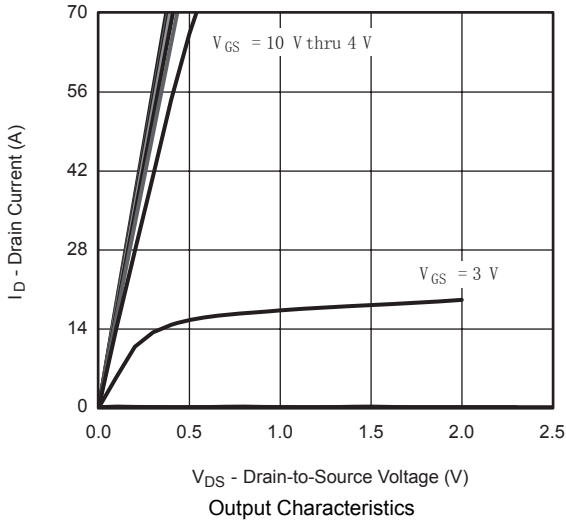
■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μA, V <sub>GS</sub> =0V	40			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	μA	
		V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5		
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	1.2		2.5	V	
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A (Note.1)			7.5	mΩ	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A (Note.1)			9		
On State Drain Current	I <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V (Note.1)	50			A	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =20A (Note.1)		70		S	
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =20V, f=1MHz		1785		pF	
Output Capacitance	C <sub>oss</sub>			264			
Reverse Transfer Capacitance	C <sub>rss</sub>			120			
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	0.2		1.6	Ω	
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =20V, I <sub>D</sub> =20A			48	nC	
		V <sub>GS</sub> =4.5V, V <sub>DS</sub> =20V, I <sub>D</sub> =20A			25		
Gate Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =20V, I <sub>D</sub> =20A		4.5			
Gate Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =20V, I <sub>D</sub> =20A		5.6			
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>DD</sub> = 20 V, R <sub>L</sub> = 2 Ω I <sub>D</sub> = 10 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω			35	ns	
Turn-On Rise Time	t <sub>r</sub>				145		
Turn-Off DelayTime	t <sub>d(off)</sub>				55		
Turn-Off Fall Time	t <sub>f</sub>				22		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>DD</sub> = 20 V, R <sub>L</sub> = 2 Ω I <sub>D</sub> = 10 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω			18	ns	
Turn-On Rise Time	t <sub>r</sub>				20		
Turn-Off DelayTime	t <sub>d(off)</sub>				40		
Turn-Off Fall Time	t <sub>f</sub>				16		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 10 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C			40	nC	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>				25		
Reverse Recovery Fall Time	t <sub>a</sub>			13			ns
Reverse Recovery Rise Time	t <sub>b</sub>			9			
Maximum Body-Diode Continuous Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			40	A	
Pulse Diode Forward Current	I <sub>SM</sub>				70		
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =4A, V <sub>GS</sub> =0V			1.2	V	

Note.1: Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.

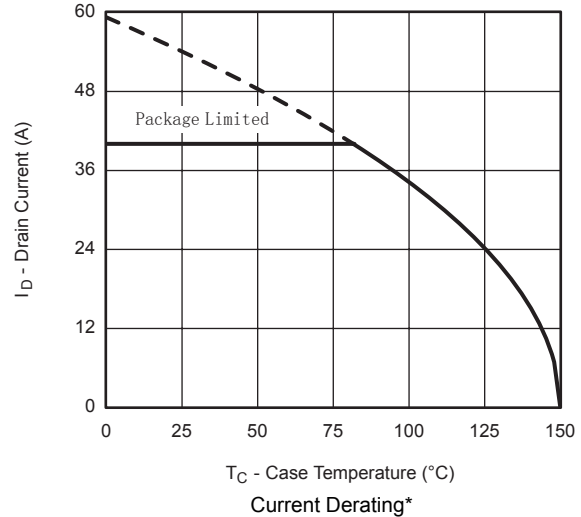
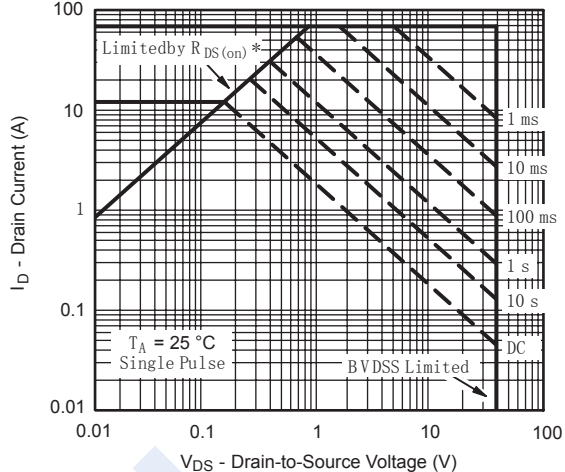
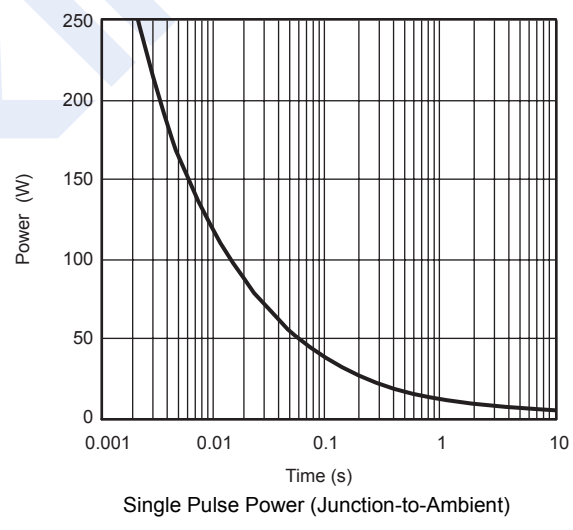
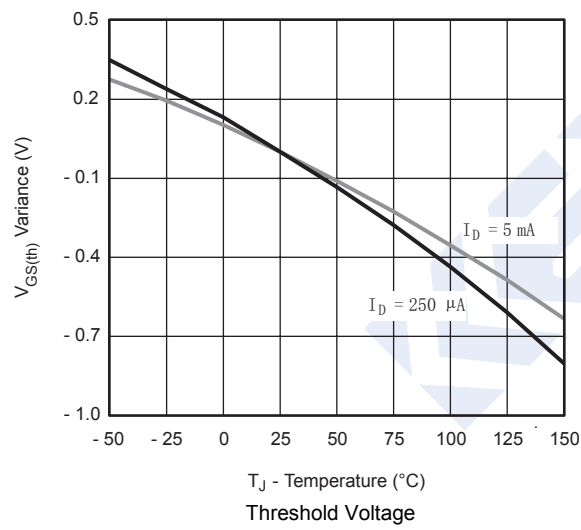
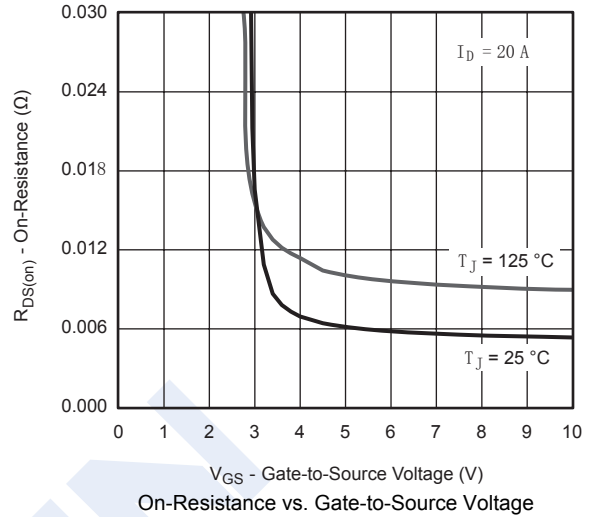
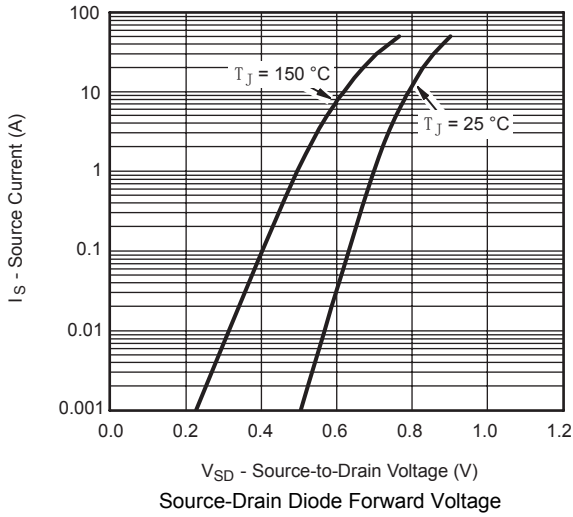
## N-Channel MOSFET SIR422DP (KIR422DP)

### Typical Characteristics



## N-Channel MOSFET SIR422DP (KIR422DP)

### Typical Characteristics

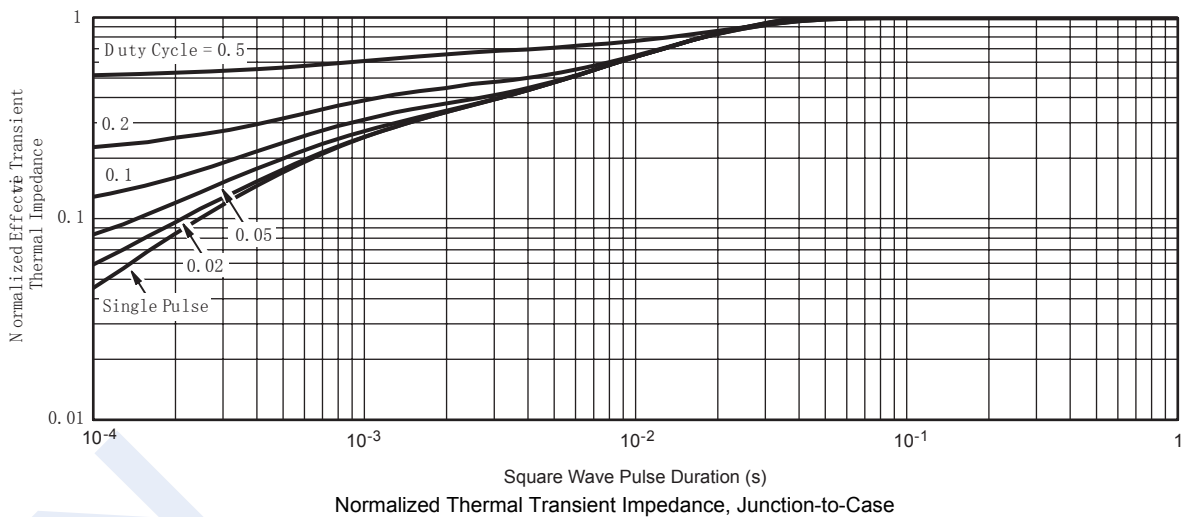
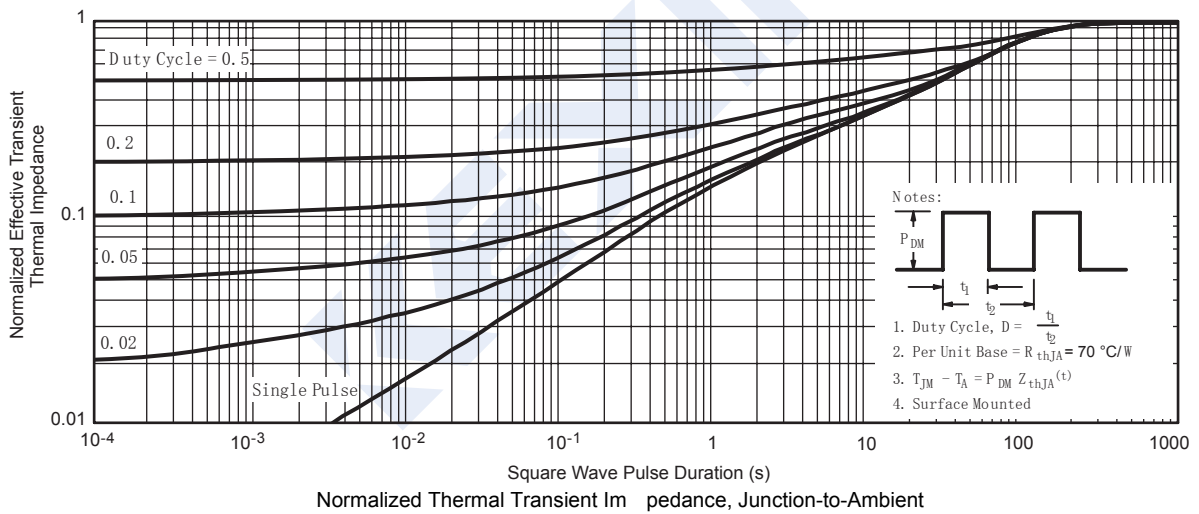
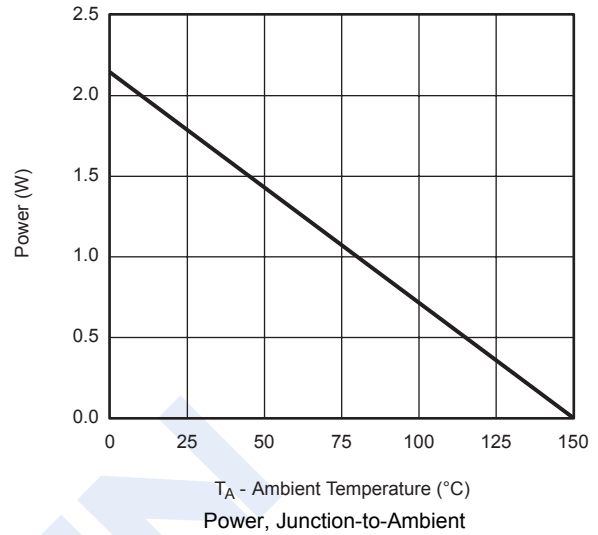
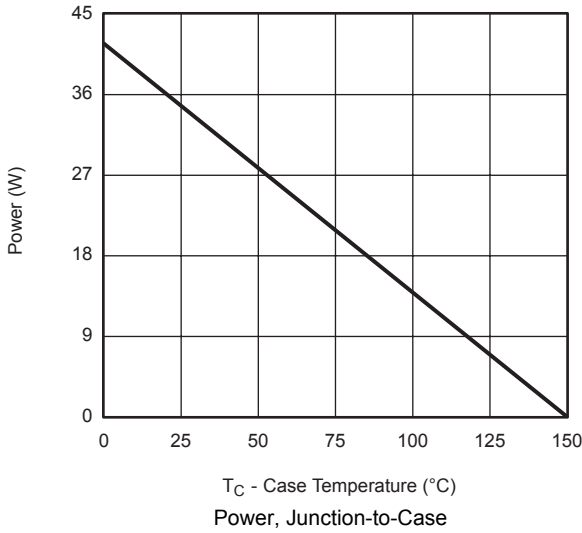


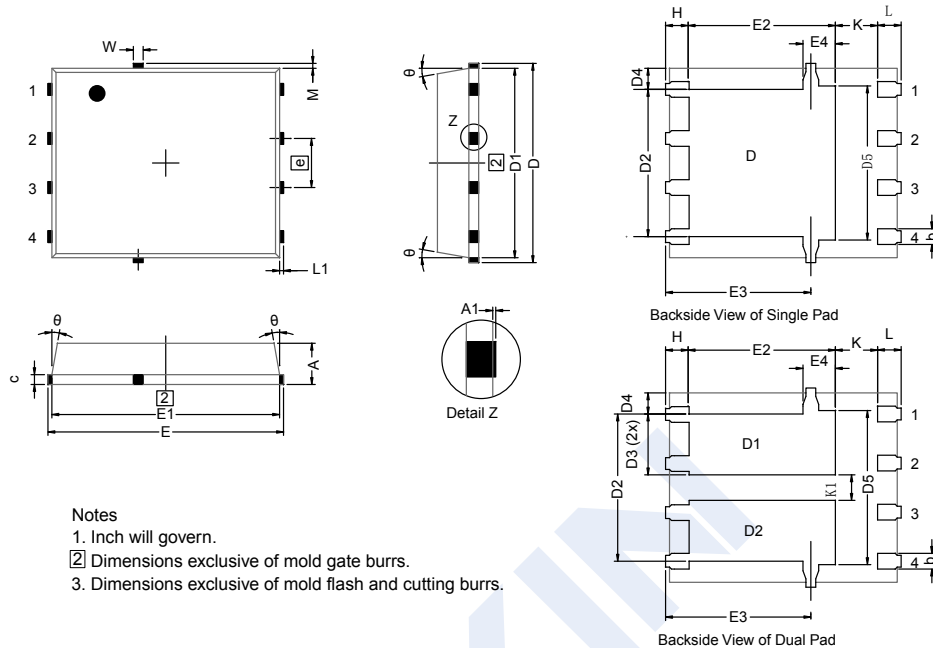
\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

# N-Channel MOSFET

## SIR422DP (KIR422DP)

■ Typical Characteristics



PowerPAK<sup>®</sup> SO-8(DFN5X6), (Single/Dual)

## Notes

1. Inch will govern.
2. Dimensions exclusive of mold gate burrs.
3. Dimensions exclusive of mold flash and cutting burrs.

DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.97	1.04	1.12	0.038	0.041	0.044
A1		-	0.05	0	-	0.002
b	0.33	0.41	0.51	0.013	0.016	0.020
c	0.23	0.28	0.33	0.009	0.011	0.013
D	5.05	5.15	5.26	0.199	0.203	0.207
D1	4.80	4.90	5.00	0.189	0.193	0.197
D2	3.56	3.76	3.91	0.140	0.148	0.154
D3	1.32	1.50	1.68	0.052	0.059	0.066
D4	0.57 typ.			0.0225 typ.		
D5	3.98 typ.			0.157 typ.		
E	6.05	6.15	6.25	0.238	0.242	0.246
E1	5.79	5.89	5.99	0.228	0.232	0.236
E2 (for AL product)	3.30	3.48	3.66	0.130	0.137	0.144
E2 (for other product)	3.48	3.66	3.84	0.137	0.144	0.151
E3	3.68	3.78	3.91	0.145	0.149	0.154
E4 (for AL product)	0.58 typ.			0.023 typ.		
E4 (for other product)	0.75 typ.			0.030 typ.		
e	1.27 BSC			0.050 BSC		
K (for AL product)	1.45 typ.			0.057 typ.		
K (for other product)	1.27 typ.			0.050 typ.		
K1	0.56	-	-	0.022	-	-
H	0.51	0.61	0.71	0.020	0.024	0.028
L	0.51	0.61	0.71	0.020	0.024	0.028
L1	0.06	0.13	0.20	0.002	0.005	0.008
theta	0°	-	12°	0°	-	12°
W	0.15	0.25	0.36	0.006	0.010	0.014
M	0.125 typ.			0.005 typ.		